

HEWLETT-PACKARD COMPANY  
Intellectual Property Administration  
P. O. Box 272400  
Fort Collins, Colorado 80527-2400

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PATENT APPLICATION

ATTORNEY DOCKET NO. 200302320-1

IN THE  
UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s): Timothe Litt

Confirmation No.: 1520

Application No.: 10/034717

Examiner: Bonura, Timothy M

Filing Date: Dec 28, 2001

Group Art Unit: 2114

Title: METHOD AND APPARATUS FOR EFFICIENTLY IMPLEMENTING TRACE AND/OR LOGIC  
ANALYSIS MECHANISMS ON A PROCESSOR CHIP

Commissioner for Patents  
PO Box 1450  
Alexandria, VA 22313-1450

STATUS INQUIRY

Sir:

1. More than 35 months have passed since:☒ NEW APPLICATIONS

the filing of this application on 12/28/2001. No communication has  
been received from the Patent and Trademark Office indicating action on this application.

☐ AMENDED APPLICATIONS

the filing of a response on \_\_\_\_\_. No further communication has  
been received from the Patent and Trademark Office.

☐ APPEALED APPLICATION

The Appeal Brief was filed on \_\_\_\_\_

☐ An Examiner's Answer was mailed on \_\_\_\_\_☐ A Reply Brief to the Examiner's Answer was submitted on \_\_\_\_\_☐ ALLOWED APPLICATIONS

the mailing of FORM POL-327 and/or Examiner's Amendment on \_\_\_\_\_

2. Kindly advise the undersigned of the present status of this application, by checking the appropriate box on the next page.

- ☐ I hereby certify that this correspondence is being deposited  
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Alexandria, VA 22313-1450  
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OR

- ☒ I hereby certify that this paper is being transmitted to the  
Patent and Trademark Office facsimile number  
on 12-7-2004 703-872-9306

Typed Name: Carrie McKenney

Signature: 

Respectfully submitted,

Timothe Litt

By: 

Gerald E. Laws

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Date: 12/6/2004

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## STATUS INQUIRY REPLY

APPLICATION SERIAL NO. 10/034717 is currently☐ Assigned to Group \_\_\_\_\_ and awaits:☐ Action by the Examiner☐ Applicant's response to the Office Action mailed \_\_\_\_\_.

APPEAL NO. \_\_\_\_\_

☐ Awaiting action by the Board of Appeals and Interferences☐ Date of hearing expected \_\_\_\_\_☐ Decision expected \_\_\_\_\_